

## An Active Gain Control System of Avalanche Photo-Diodes for Phase-Shift Laser Range Finder

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**Abstract.** In time fight phase-shift laser range finder, it is noticed that unwanted phase delay in avalanche photo-diodes (APD) between the incident light and the output current. When the modulation frequency is higher and the incident light is weaker, the phase delay is becoming larger. The measurement resolution of laser range finder by the optical modulation method is strongly influenced by the undesirable phase delay. In order to solve the problem, the relationships among the phase delay, the reverse bias voltage and the multiplication factor of APD were investigated at different modulation frequency respectively. An active gain control system was proposed by controlling the reverse bias acting on the APD according to different incident light power. Given the high light intensity, the reverse bias voltage was adjusted to keep APD operating at optimal M factor, and the phase delay was compensated by software, on the condition that the low light intensity, the reverse bias voltage is decreased to reduce the phase delay. The experiment results show that for a modulation frequency of 75MHz and incident light intensity of 0.5uW, the phase delay can be reduced from  $4.8013^\circ$  to less than  $0.04^\circ$ .

### Introduction

APD is a promising light sensor for various military fields such as laser range finder, laser radar, and laser identification of friend or foe because of its internal gain and high quantum efficiency, which can greatly reduce the performance requirement of pre-amplification circuits, and can improve the SNR of detecting circuits. Many researchers found that in case of pulse laser radar where the APD is exposed to wide dynamics of input pulses causing error in pulse time of flight measurement[1][2]. At the same time, in phase-shift laser range finder, with the increasing demand of accuracy in such applications, the modulation frequency is increasing higher up to hundreds' of MHz; the nonlinearity of APD is becoming a hot research issue in phase-shift laser range finder[3][4]. Shuko Yokoyama and Akito Okamoto found that the degree of the phase delay increase with a decrease of the incident intensity, the distance measurement error will increase greatly when the incident light is weak; they concluded that the phase delay can be reduced by superimposing a bias light on the modulation light so that the total intensity remains constant[5]. Other researchers found that the phase delay is related to the light modulation frequency (or rising edge of the light pulse) and many correction systems were proposed in pulse laser range finder [6]-[10]. To clarify phase delay in phase-shift laser range finder, in our paper, a system has been designed, implemented and experimentally tested, the relationships among the phase delay, the reverse bias voltage acting on APD and the APD M factor. According to the experiment results, a system adjusting the reverse bias voltage flexibly was proposed that can not only suppress the phase delay but also ensures the SNR of the laser range finder.

### APD nonlinearity and phase delay measurement system

#### 1.1 APD Nonlinearity

The response of receive system in laser range finder is decided by the intrinsic response time of the APD and the RC time constant of the receive circuits. If the receive circuits is designed, the RC

time constant is stable, and can be compensated. The intrinsic response time  $\tau_r$  of the APD photoelectric signal is expressed by:

$$\tau_r = \tau_e + \tau_h + \tau_m \quad (1)$$

Where  $\tau_e$  and  $\tau_h$  is the transit time of electrons and holes through the absorption layer respectively, and  $\tau_m$  is the multiplication time of the carriers. They can be expressed as:

$$\tau_e = \frac{d_a}{v_e} \quad (2)$$

$$\tau_h = \frac{d_a}{v_h} \quad (3)$$

$$\tau_m = kM \frac{d_g}{v_e} + \frac{d_g}{v_p} \quad (4)$$

Where  $d_a$  is the absorption layer thickness,  $v_e$  and  $v_h$  are the saturation velocity of electrons and holes,  $k$  is the ionization ratio and  $d_g$  is the multiplication layer thickness of the APD. Even the multiplication time  $\tau_m$  is lower than transit time  $\tau_e + \tau_h$ , but the phase delay is mainly due to the multiplication time [4]. If the structure of APD is set, and applied electric field is stable,  $\tau_m$  increases with M factor, the relationship can be evaluated by:

$$\delta t = k \cdot \delta M \cdot \frac{d_g}{v_e} \quad (5)$$

So in phase-shift laser range finder, the phase-shift variation is mainly due to the M factor variation:

$$\delta \varphi = 2\pi f_k k \cdot \delta M \cdot \frac{d_g}{v_e} \quad (6)$$

Where,  $f_k$  is the intensity modulation frequency in phase-shift laser range finder. In this case, the operation of APD as a receiver in laser range finder is shown in Fig. 1.

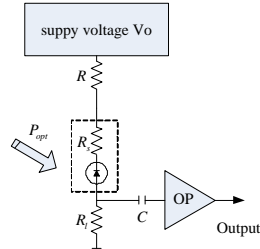


Fig. 1 Operation of APD as a receiver in laser range finder

The value of M factor is given by the inverse bias voltage  $V_H$  acting on APD and breakdown voltage  $V_B$  of the APD as:

$$M(i) = \frac{1}{1 - \left( \frac{V_H}{V_B} \right)^n} \quad (7)$$

Where n is the concavity index of the avalanche zone and is 1.4-2.0 for silicon APD. According to Fig. 1:

$$V_H = V_o - (R + R_s + R_L) i_{ph} \quad (8)$$

Where  $V_o$  is the supply voltage output,  $R$  is the current-limiting resistance,  $R_s$  is the APD inner resistance,  $R_L$  is the load resistance,  $i_{ph}$  is the average photocurrent, which decreases with the decreases of incident light power.

From equation (7) and (8), we can deduce that  $V_H$  increases with incident light power decreasing, and the M factor increases. Combine with equation (6), the increase in phase delay can be explained by the decrease in the incident light intensity. And if the modulation frequency  $f_k$  is higher, the phase delay will be larger which leads to the distance measurement error increase.

## 1.2 Phase delay measurement system

From A section, we can conclude that the phase delay increase with the increasing of the modulation frequency and decreasing of the incident light power. To observe the phase delay by the nonlinearity of APD, the following system is designed in Fig. 2. The bias voltage is produced by PA92 and controlled by DSP through DAC, supplying APD1 and APD2 with variable high voltage at the same time (APD, Silicon Sensor, AD230-8, -3dB cutoff frequency 2GHz ). A laser diode (LD) is driven by a sinusoidal current at modulation of 75MHz and 7.5MHz.

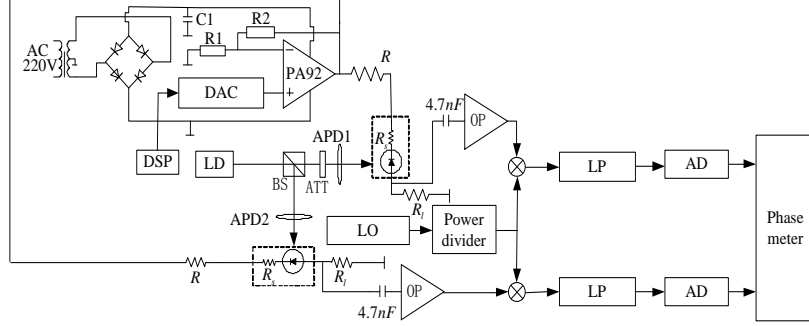


Fig. 2 Diagram of measuring phase error caused by nonlinear characterization of APD

Light from LD was divided into a reference beam illuminating the APD2 at fixed intensity of  $50\mu\text{W}$  and a measurement beam on APD1 which intensity is adjustable between  $0.5\mu\text{W} \sim 50\mu\text{W}$  by ATT. Local Oscillator (LO) signal after the power divider is mixed with measurement signal and the reference signal in convenient of the phase shift measurement. Two signals are sampled by ADC to phase meter employing apFFT method. The phase delays are shown in at modulation frequency of 75MHz and 7.5MHz when the bias voltage is set between 50V~147V.

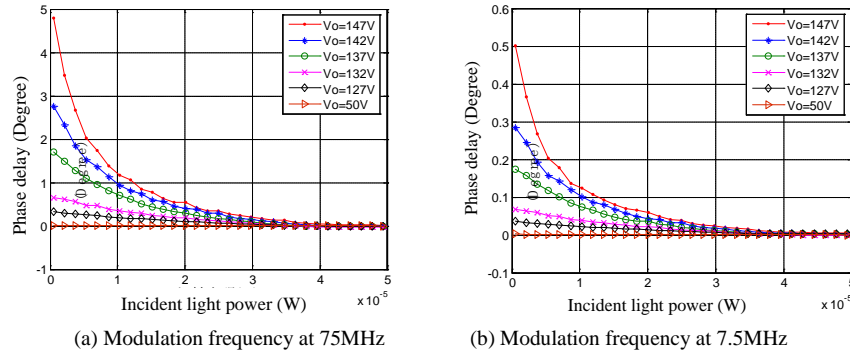


Fig. 3. Relation of incident light intensity and phase error

The maxim value of phase delay is  $4.8013^\circ$  and  $0.5018^\circ$  at modulation frequency of 75MHz and 7.5MHz respectively. The phase delay is increasing with the decreasing of light intensity, and at fixed light intensity, the phase delay is decreasing with the decrease of bias voltage  $V_o$ . when the bias voltage is set at 50V, the phase delay is near zero. It has been proven that APD is a single pole system, the cutoff frequency is  $f_c$ , so:

$$f_c M = k (: \text{constant}) \quad (9)$$

For a phase-shift laser range finder at modulation frequency  $f_k$ , the phase delay can be written as:

$$\Delta\phi_d = \arctan \frac{f_k}{f_c} = \arctan \frac{f_c M}{k} \quad (10)$$

According to equation (6) and (10), the phase delay is decided by the M factor. The Fig.4 shows plot of the incident light intensity and multiplication factor  $M_{act}$  for  $V_o = 127\text{V} \sim 147\text{V}$ , the multiplication factor is unity when  $V_o = 50\text{V}$ .

$$M_{act} = \frac{I_{ph} / V_o = V_s}{I_{ph} / V_o = 50\text{V}} \quad (11)$$

Where  $I_{ph}$  is the average photocurrent.

The  $M_{act} \doteq 20$  with the increasing of incident light intensity. The M factor is increasing with the decrease of incident light power at fixed bias voltage. And reduce the bias voltage can make the M

factor become a constant, which make the phase delay compensation become easy. So we can suppress the phase delay by adjusting the bias voltage, and if the bias voltage cannot be adjusted flexibly, the phase delay can be calculated according to equation (9) and (10) and compensated.

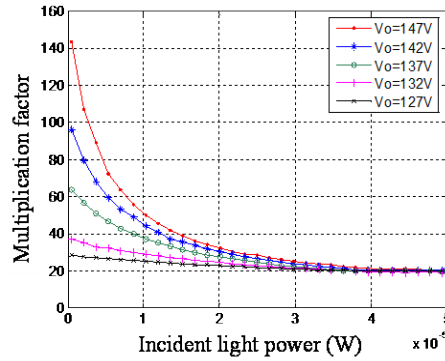


Fig.4. Relationship of incident light intensity and at different bias voltages

## Control system design by adjusting bias voltage

### 2.1 SNR of system and optimal M factor

From section 2, we know that lower the bias voltage to keep the M factor be a constant can reduce phase delay. But it is a conflict between M factor and high SNR of the receive system. The relationship between M factor and the SNR is discussed in order to establish the bias voltage control system. The SNR of current can be calculated by the following equation:

$$SNR = \frac{i_{photo}}{i_n} = \frac{M \cdot I_{ph}}{\left\{ \bar{i}_{Tn}^2 + 2eB \cdot \left[ I_{DS} + (I_{DB} + I_{ph}) \cdot M^2 \cdot F(M) \right] \right\}^{\frac{1}{2}}} \quad (12)$$

$$\bar{i}_n^2 = \bar{i}_{Tn}^2 + \bar{i}_{DB}^2 + \bar{i}_{DS}^2 + \bar{i}_{pn}^2 \quad (13)$$

$$\begin{cases} \bar{i}_{Tn}^2 = 4KTB/R_L \\ \bar{i}_{DB}^2 = 2eM^2 \cdot F(M) \cdot B \cdot I_{DB} \\ \bar{i}_{DS}^2 = 2e \cdot I_{DS} \cdot B \\ \bar{i}_{pn}^2 = 2e \cdot S(\lambda) \cdot P_r \cdot M^2 \cdot F(M) \cdot B \end{cases} \quad (14)$$

Where  $k$  is Boltzmann constant,  $T$  is temperature of detector's load resistance  $R_L$ ,  $B$  is the bandwidth of the receive system,  $e$  is electron charge of  $1.602 \times 10^{-19} C$ ,  $M$  is multiplication factor,  $P_r$  is incident light intensity,  $F(M)$  is APD excess noise factor, and for reach through structure of silicon APD,  $F(M)$  is given by:

$$F = kM + \left( 2 - \frac{1}{M} \right) (1 - k) \quad (15)$$

The M factor and output SNR of APD is plotted in Fig. 5.

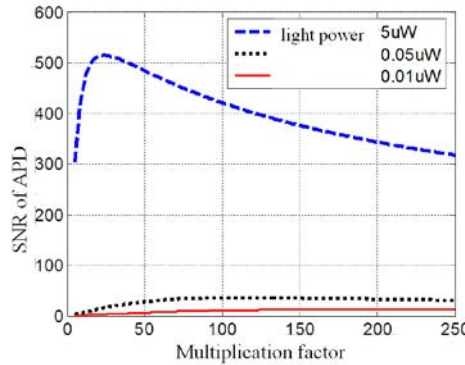


Fig. 3. Curve of multiplication and output SNR of APD

## 2.1 Reducing phase delay by bias voltage adjusting and software compensating

The SNR is affected by M factor when the incident light is intense. When the incident light intensity is weak, the optimal M factor is relatively constant. So we establish the control strategy as follows: adjusting the bias voltage to make the APD reaching the optimal SNR when the incident light is intense. When the incident light intensity is weak, we adjust the bias voltage to keep the M factor at a constant value, the phase delay can be compensated by the optimal M factor according to equation (10). The control system is presented in Fig. 6. The bias voltage can be controlled by DSP and a DAC. The adjusting procedure is shown in Fig. 7.

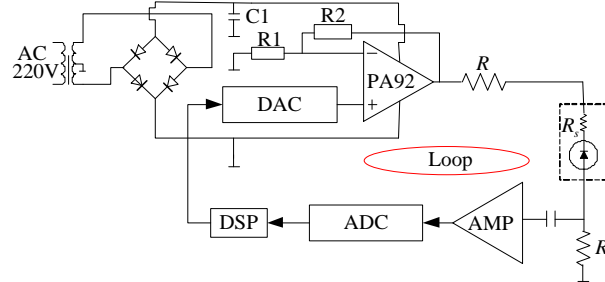


Fig. 4 Multiplication factor control system

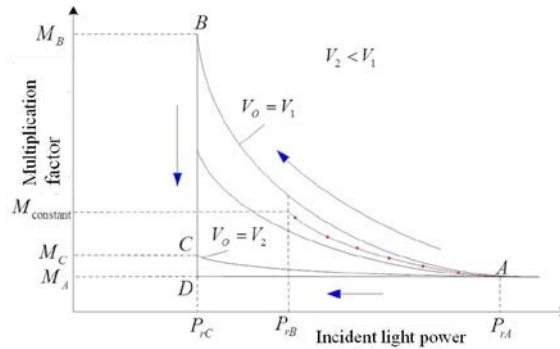


Fig. 5 Procedure of reduce the phase error by adjusting the bias voltage

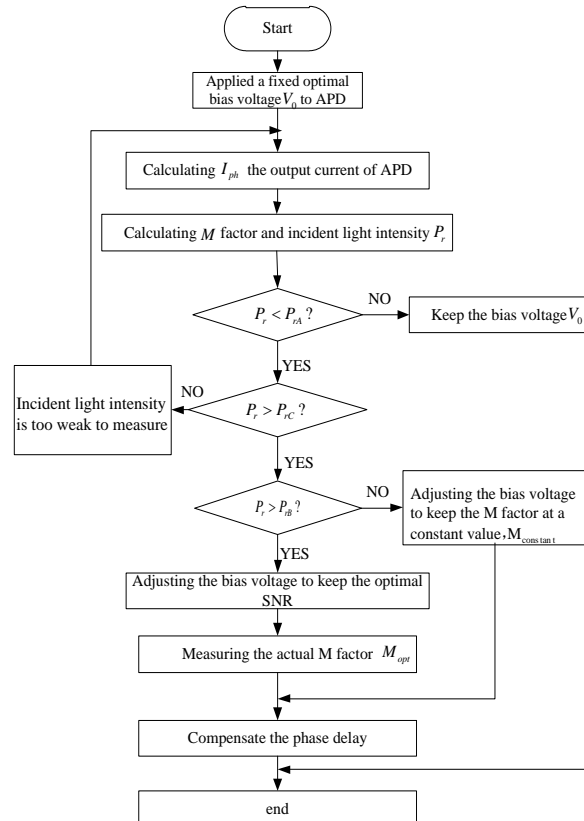


Fig. 6. Flowchart of reducing phase delay by multiplication control and software compensating

Two curves AB and AC belong to the different bias voltage  $V_1$  and  $V_2, V_2 < V_1$ . when incident light intensity decrease from A point to D point at bias voltage  $V_1$ , the M factor increases from  $M_A$  to  $M_B$  along the curve AB. If we control the bias voltage reduce from  $V_1$  to  $V_2$ , the M factor jumps from  $M_B$  down to  $M_C$ , the phase delay is reduced. In order to reduce the phase delay and keep the optimal SNR of the system at the same time, we work out control scheme according to the incident light intensity  $P_r$ . The flowchart of our control system is shown in Fig. 8.

## Experimental results

We change the incident light on APD1 by ATT according to Fig. 2 in  $0.5\mu\text{W} \sim 50\mu\text{W}$  at modulation frequency of 75MHz and 7.5MHz. The phase delay is measured after bias adjusting and software compensating. The experimental results are shown in Table 1. experimental results show that the phase delay caused by APD nonlinearity is less than the  $0.04^\circ$ .

Table 1 Compensation results of phase delay caused by nonlinear of APD at different modulation frequency

Modulation frequency $f = 75\text{MHz}$		Modulation frequency $f = 7.5\text{MHz}$	
$P_r (\mu\text{W})$	Phase delay(Degree)	$P_r (\mu\text{W})$	Phase delay(Degree)
0.50	0.0293	0.52	0.0245
0.81	0.0301	0.84	0.0287
1.08	0.0214	1.35	0.0154
4.09	0.0300	4.49	0.0134
8.42	0.0252	8.94	0.0291
13.06	0.0245	13.72	0.0223
17.03	0.0223	17.82	0.0175
25.32	0.0187	25.23	0.0226
33.92	0.0121	33.96	0.0151
41.84	0.0000	41.01	0.0000

## Conclusion

We have developed an active bias voltage control system combine with software compensation method that can not only reduce the phase delay caused by the nonlinearity of APD but also keep the receive system keep its optimal SNR. The nonlinearity of APD is presented, the relationship among phase delay, multiplication factor, and the incident light intensity are analyzed. The experimental results show that our control system is feasible, flexible, and easier to implement.

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